ast Search	L No.	Hits	Text Search		Data Bases
Search	L NO.	iiits	("257/81,84,88,92,93,98,99,100,690,698		USPAT; EPO; JPO;
IS&R	1	5967	,700").CCLS.	9/7/01 11:01	Derwent; IBM TDB
ISAK	 	3707	1 and (array (light adj displace adj		USPAT; EPO; JPO;
BRS	,	989	panel))	9/7/01 11:02	Derwent; IBM TDB
DIG	 	1 707	pane.//		USPAT; EPO; JPO
BRS	3	557	2 and (ic (integrated adj circuit))	9/7/01 11:03	Derwent; IBM TDB
DIG	 	+	3 and (ic (integrated adj circuit))) and		USPAT; EPO; JPO
BRS	4	30	transparent adj substrate	9/7/01 11:05	Derwent; IBM TDE
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USPAT		Page		Title	Cl/Sub	Cl/Sub	Inventor
	Date	Page				257/103; 257/443;	
						257/448; 257/466;	
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			Ontoelectronic	semiconductor diodes and		257/88; 257/89;	
10 5025007 4	19990720		devices compri		257/80	257/99:313/500	Oberman, David B.
JS 5925897 A	19990720	23	devices compri	Sing suite		257/82; 257/98;	
			Microelectroni	c module having optical		359/15 ; 359/34 ;	
10 5022706 4	19990713	15	and electrical in		385/14	385/37	Feldman, Michael R., et a
US 5923796 A	19990/13		Structure and f	abrication method for			
				g light emitting diodes		257/81; 257/88;	
				ion extending through vias		257/98; 257/99;	
			in a polymer fi			345/82; 345/84;	1
UC 6006401 A	19990323	6	emitting diodes		257/678	359/34 : 385/14	Liu, Yung Sheng
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US 5621225 A	19970415	10	 I ight emitting	diode display package	257/81	257/88; 257/99	Shieh, Chan-Long, et al.
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			High resolution	n active matrix LCD cell		257/99; 349/149;	
US 5536950 A	19960716	12	design		257/59	349/43	Liu, Michael S., et al.
US 3330930 A	19900710	12	Light emitting	diode array with aligned			
US 5060027 A	19911022		solder bumps	diodo antaj managana	257/88	257/779 ; 257/99	Hart, Peter B., et al.
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BRS	L9		light adj emitting adj (element device	9/7/01 14:37	USPAT; EPO; JPO; Derwent; IBM TDB
BRS	L10	6531	9 and printed adj circuit adj board	9/7/01 14:38	USPAT; EPO; JPO; Derwent; IBM TDB USPAT; EPO; JPO;
BRS	L12	2101	10 and array	9/7/01 15:25	Derwent; IBM TDB USPAT; EPO; JPO;
BRS	L13	8	12 and reflect\$3 adj electrode	9/7/01 15:32	Derwent; IBM TDB USPAT; EPO; JPO;
BRS	L14	3	13 and transparent adj substrate	9/7/01 15:34	Derwent; IBM TDB

Search Result	T		Title	Cl/Sub	Cl/Sub	Inventor
USPAT	Date	Page	Little	Chan	349/138 ; 349/42 ;	Inventor
1			TO 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		349/59; 349/62;	
			Display panel and display device using a	240/140	1349/84	Takahara, Hiroshi, et al.
US 5673127 A	19970930	72	display panel	349/140	349/84	Takanara, rinosin , ct ai.
			Original illumiating device and original			
			reading device preventing random		240/225	Name dispersioni et al
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			Optical semiconductor device and optical		257/433; 257/436;	1
			semiconductor module equipped with		257/80 ; 257/82 ;	l
US 6252252 B1	20010626	25	the same	257/81	438/116	Kunii, Hideo, et al.
			Optical distribution of transmitter signals			!
			and antenna returns in a phased array			
US 4885589 A	19891205	13	radar system	342/175	342/368	Edward, Brian J., et al.
00 10000077			Hybrid integrated circuit device			
			comprises substrate having surface with			
			insulation, electrodes formed on			
EP 1059678			substrate surface and light emitting			
A2	20010326	16	element		<u> </u>	IN
AZ	20010320				257/88; 257/91;	
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US 5134340 A	19920728	1 12	Light-emitting diode printhead	313/500	438/28	Haitz, Roland H.
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